**Digital technics (ECTS 6)**

Language: the course is offered in English, Serbian and Hungarian

Contact person: Dr. Lívia Szedmina (slivia@vts.su.ac.rs)

**Course description:**

This course covers the following topics:

Boolean algebra. Laws of logic. Number systems. Combination networks. Minimize functions. Sequential networks. Registers and counters. Technical characteristics of logic elements. Structure of modern microprocessors and microcontrollers.

**Aims:**

* To learn methods of analysis and synthesis of combining networks.
* To acquire the knowledge of the methods of analysis and synthesis of sequential networks.
* To study real parameters of digital systems.
* To learn the basic elements of microprocessor based systems.